

L Number	Hits	Search Text	DB	Time stamp
1	8106	system adj2 chip	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:20
2	3903	ring adj network	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:20
3	88	(system adj2 chip) and (ring adj network)	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:20
4	88	processor and ((system adj2 chip) and (ring adj network))	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:21
5	57	dma and (processor and ((system adj2 chip) and (ring adj network)))	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:24
6	74935	memory near3 controller	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:24
7	78	(processor and ((system adj2 chip) and (ring adj network))) and (memory near3 controller)	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:28
8	509	(dma and processor and memory adj2 controller) and (system adj2 chip)	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:31
9	88	soc and ((dma and processor and memory adj2 controller) and (system adj2 chip))	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:38
10	159	(processor and ((system adj2 chip) and (ring adj network))) or (soc and ((dma and processor and memory adj2 controller) and (system adj2 chip)))	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:38
11	114	clock and ((processor and ((system adj2 chip) and (ring adj network))) or (soc and ((dma and processor and memory adj2 controller) and (system adj2 chip))))	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:50
12	0	myers,p\$.xp.	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:51
13	265	myers-p\$.xp.	USPAT; US-PGPUB; EPO; JPO	2004/08/31 17:51
14	3	myers-p\$.xp. and (clock and ((processor and ((system adj2 chip) and (ring adj network))) or (soc and ((dma and processor and memory adj2 controller) and (system adj2 chip))))	USPAT; US-PGPUB; EPO; JPO	2004/08/31 18:00
15	3884	bus adj clock	USPAT; US-PGPUB; EPO; JPO	2004/08/31 18:00
16	10	(bus adj clock) and ((processor and ((system adj2 chip) and (ring adj network))) or (soc and ((dma and processor and memory adj2 controller) and (system adj2 chip))))	USPAT; US-PGPUB; EPO; JPO	2004/08/31 18:01